



TELECOMMUNICATIONS:

• ISDN SO-INTERFACE TRANSFORMERS AND CHOKES FOR PCMCIA - APPLICATIONS:

- The series of transformers is specifically intended for ISDN BRI-4wire s₀-interfaces defined by CCITT(ITU-T)I.430 recommendations for the Integrated Services Digital Network.

ISDN-IC/S₀- Interface Transformer Selection:

Chip Manufacturer	Chip Designation	Transmit	Receive
AMD	AM79C30A	UT21023	UT21023
	AM79C32A	UT28113-A	UT28113-A
		UT28113-B	UT28113-B
AT&T	T7250C	UT21024	UT21024
	T7256		
	T7259		
Mitel	MT8930	UT21023	UT21023
	MT8931	UT28113-A	UT28113-A
		UT28113-B	UT28113-B
Motorola	MC145474	UT21022	UT21022
	MC145475		
	MC145574	UT21024	UT21024
National	TP3420	UT21023	UT21023
	TP3421	UT28113-A	UT28113-A
		UT28113-B	UT28113-B
SGS Thomson	ST5420	UT21023	UT21023
	ST5421	UT28113-A	UT28113-A
		UT28113-B	UT28113-B
Siemens	PEB2080, 2081	UT21023	UT21023
	PEB2084, 2085	UT28113-A	UT28113-A
	PEB2086, 2186	UT28113-B	UT28113-B

DATA SHEET 12-21 SEP./98 1 OF 3



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Universal Microelectronics

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• **ISDN SO-INTERFACE TRANSFORMERS AND CHOKES FOR PCMCIA - APPLICATIONS:**

Electrical specifications @ 25°C:

Transformer:

UMEC Part no.	n ±2%	L _H mH Min.	L _S uH Max.	C _K pF Max.	C _W pF Max.	R _{CU.IC} NOM.	R _{CU.L} NOM.	U _P KVrms
UT21022	1split :1	22	5	70	25	2.0	1.8	1.5
UT21023	2split :1	22	5	70	25	4.0	1.8	1.5
UT21024	2.5split :1	22	5	80	45	5.3	1.8	1.5
UT28113-A	2:1	30	3	140	–	6.6	3.3	1.5
UT28113-B	2:1	30	3	140	–	6.6	3.3	1.5

Common Mode Choke:

UMEC part no.	L _N mH (+50%~-30%)	I _N mA	L _S uH Max.	R _{CU} Nom.	U _P KVrms
UT28103-A	4x4.7	150	–	0.80	0.5

Definition of symbols:

Transformer:

n= turns ratio: IC-side:Line-side.

L_H= main inductance of winding(s) on Line-side(in series, f=10KHz U=100mVrms).

L_S= leakage inductance of winding(s) on Line-side with winding(s)
on IC-side short circuited(each in series, f=100KHz U=100mVrms).

C_K= coupling capacitance between the winding(s) on IC-side
and the winding(s) on Line-side winding(in series, f=10KHz U=100mVrms).

C_W= winding capacitance of winding(s) on Line-side(in series, nominal value, f=1MHz U=1Vrms).

R_{CU.IC}= DC resistance of the winding(s) on IC-side(in series, nominal value).

R_{CU.L}= DC resistance of the winding(s) on Line-side(in series, nominal value).

U_P= test voltage, rms value 50/60Hz, 2seconds, winding(s) on Line-side to winding(s) on IC-side.

Chokes:

L_N= rated inductance of a winding(tol. +50%/-30%, f=10KHz U=100mVrms).

I_N= permissible rated current of winding.

L_S= leakage inductance of winding when all other windings
short circuited(nominal value, f=100KHz U=100mVrms).

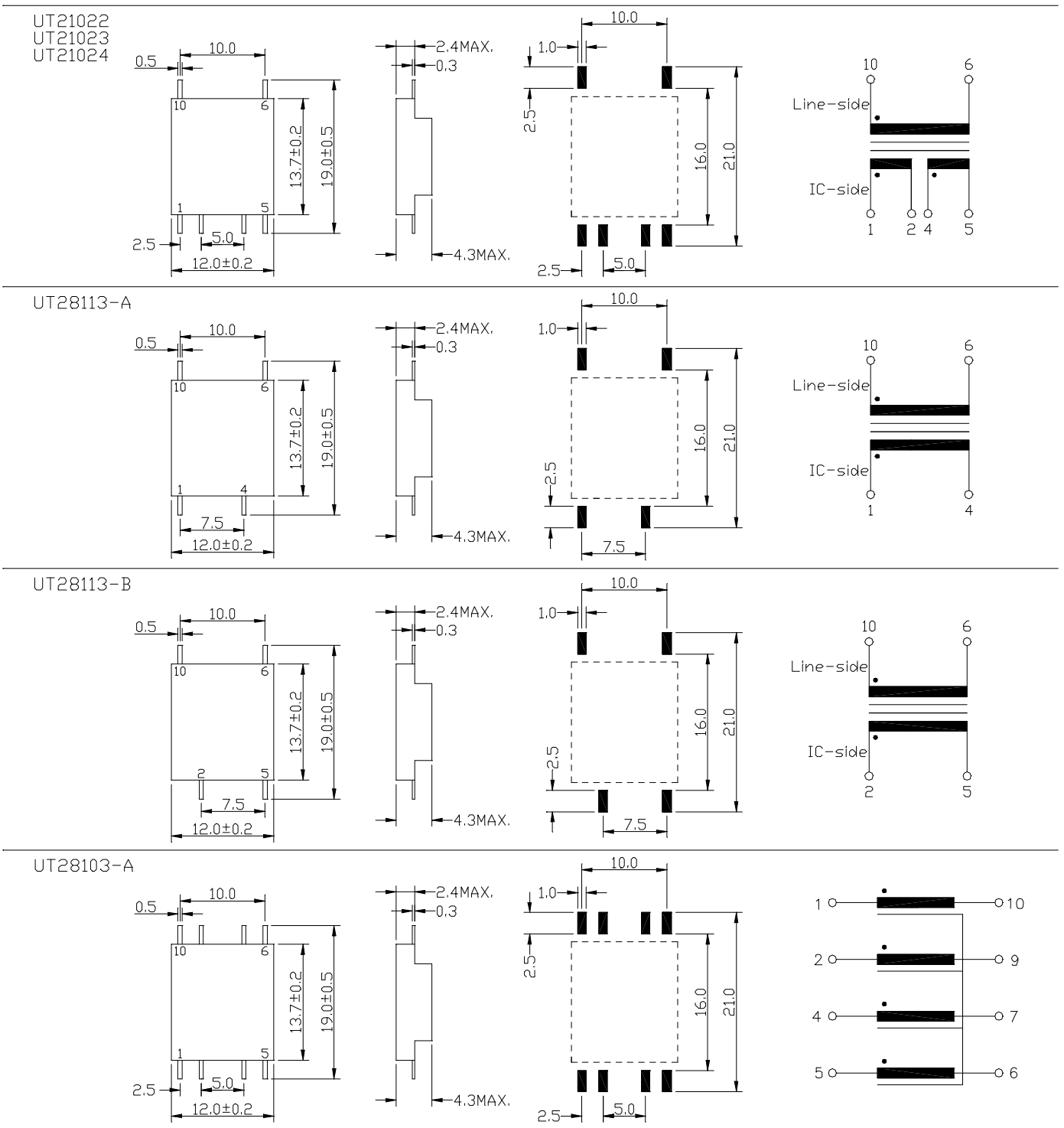
R_{CU}= DC resistance of each winding(nominal value).

U_P=test voltage, rms value 50/60Hz, 2seconds, winding to winding.



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Dimensions and connections (tolerance = ±0.2mm)



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